

# PARALLELIZATION OF APPLICATION FOR CONVERTING A NONDETERMINISTIC FINITE AUTOMATON INTO A DETERMINISTIC FINITE AUTOMATON

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## Abstract

The questions of parallelization of the algorithm for the transformation of a non-deterministic finite automaton to an equivalent deterministic finite automaton are considered. Feature of implementation is to use the CUDA hardware and software platforms, GPGPU technology. This significantly increases the productivity of the application.

## Type of Book of Abstracts

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